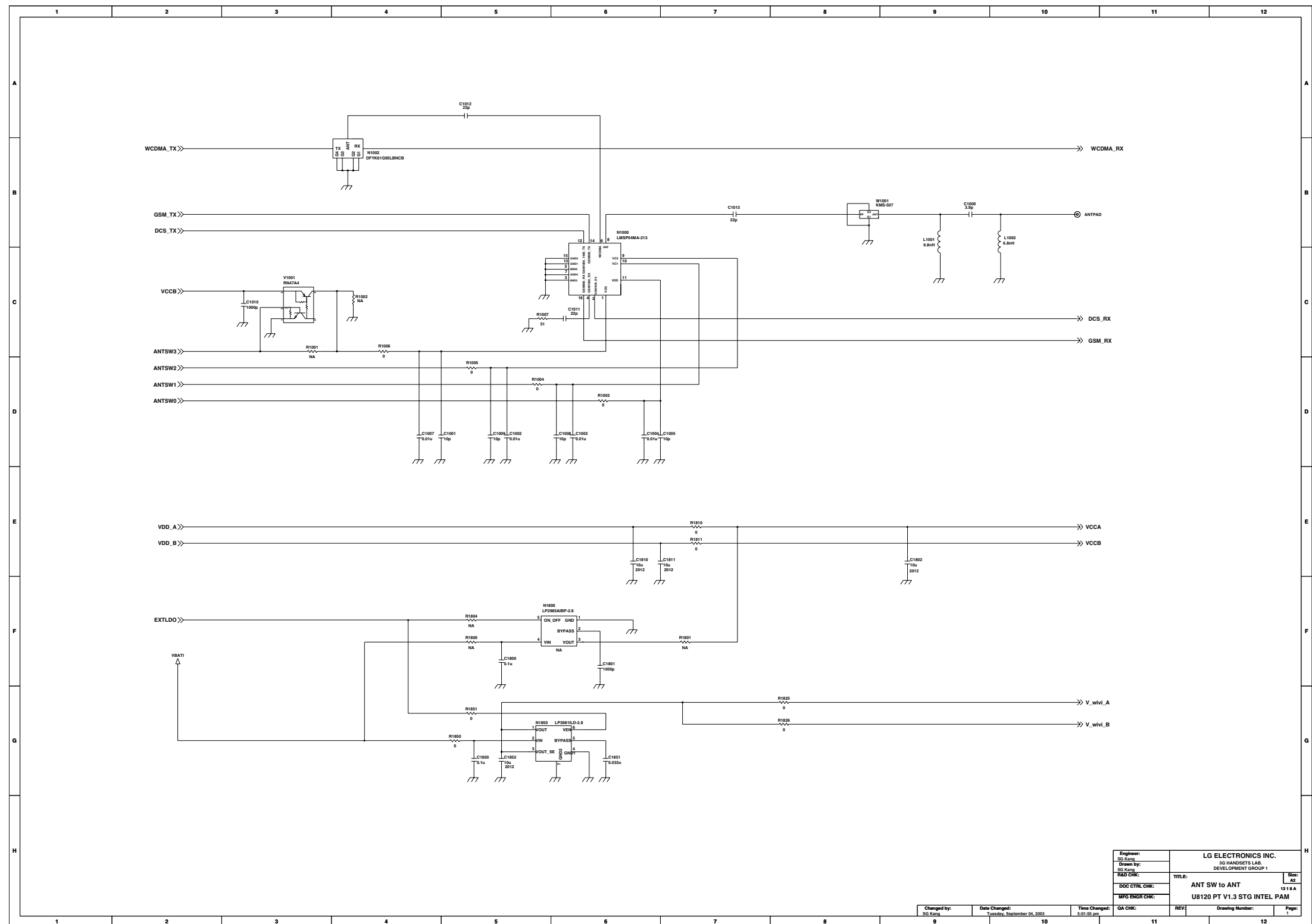
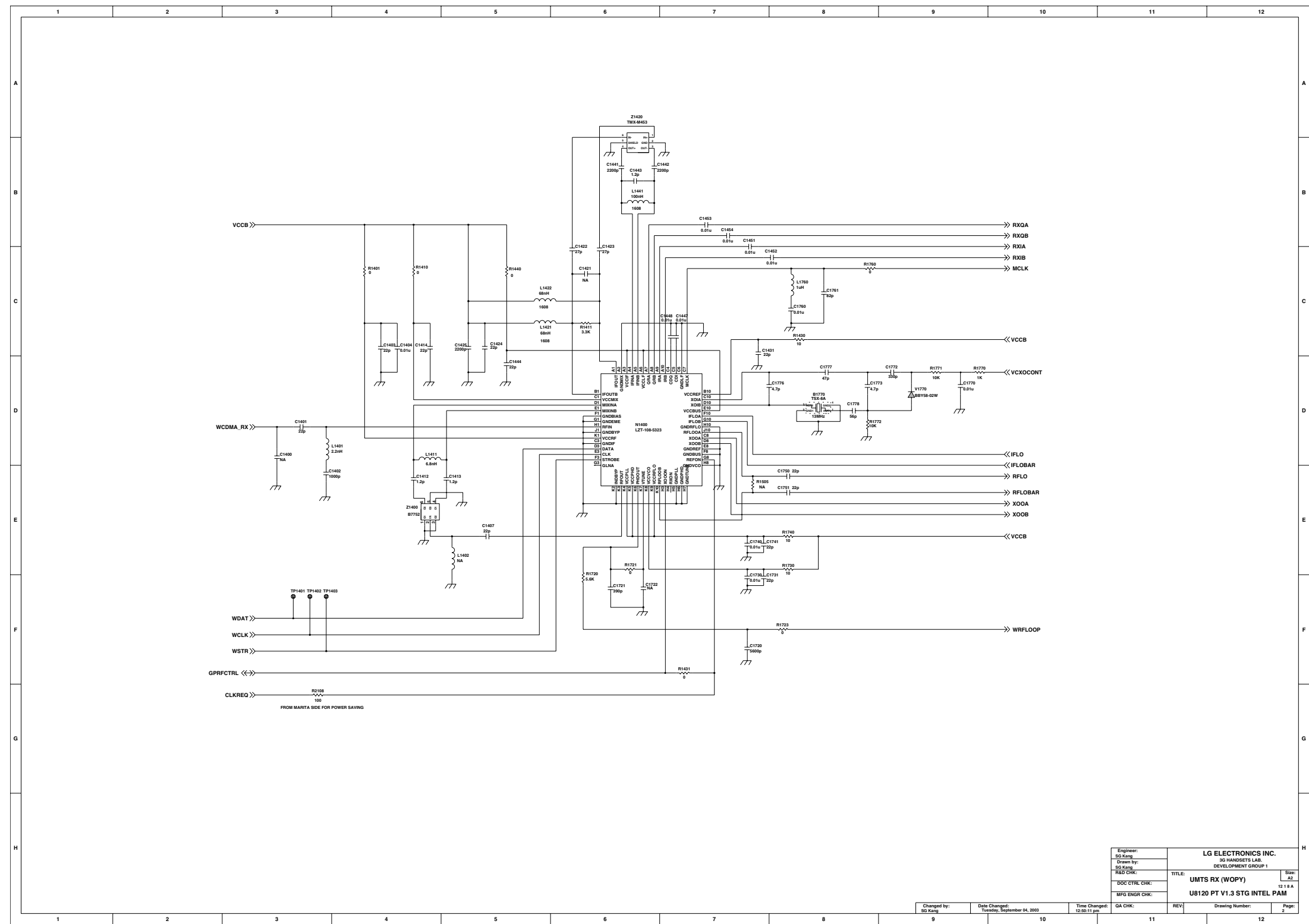


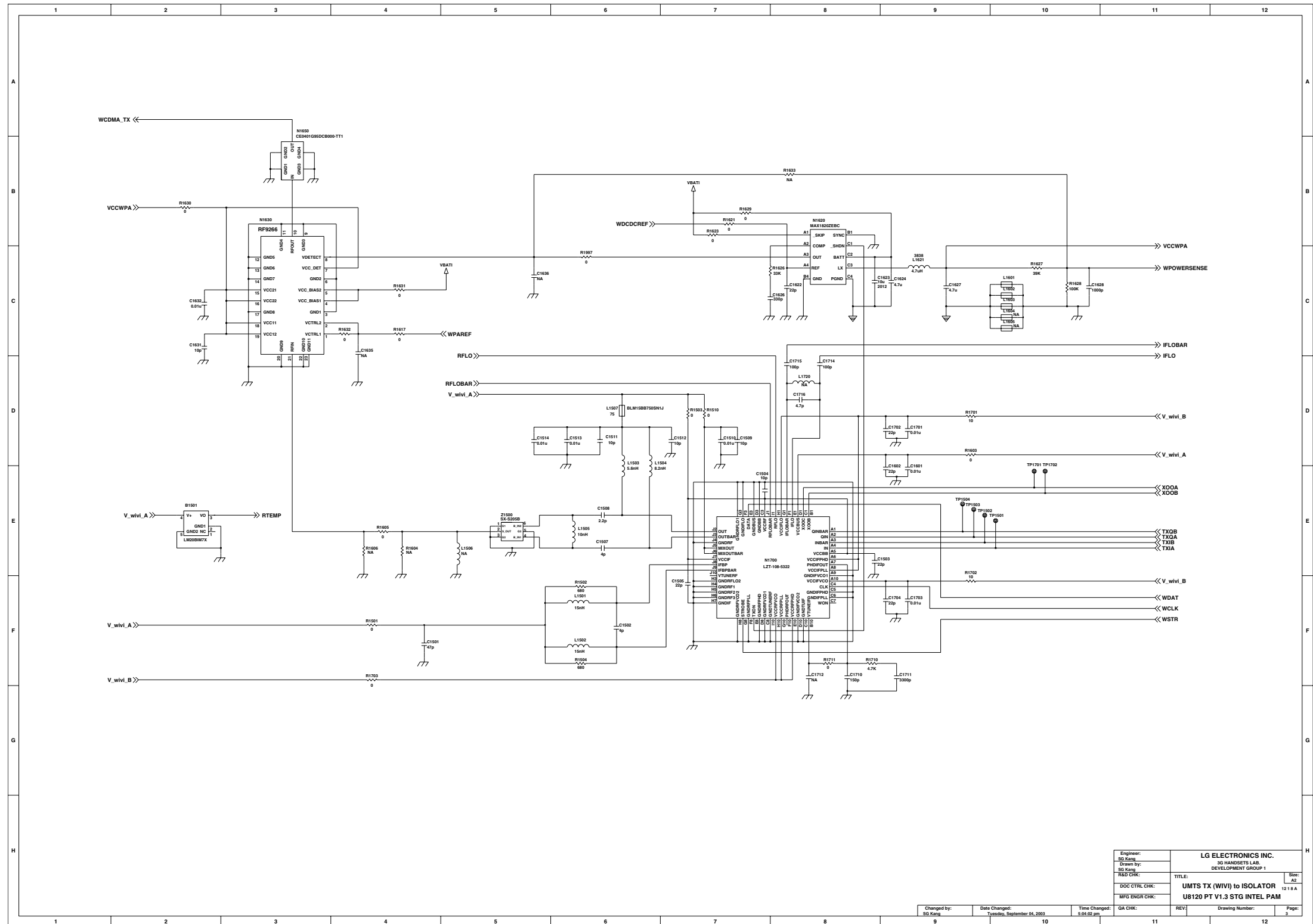
## 9. CIRCUIT DIAGRAM



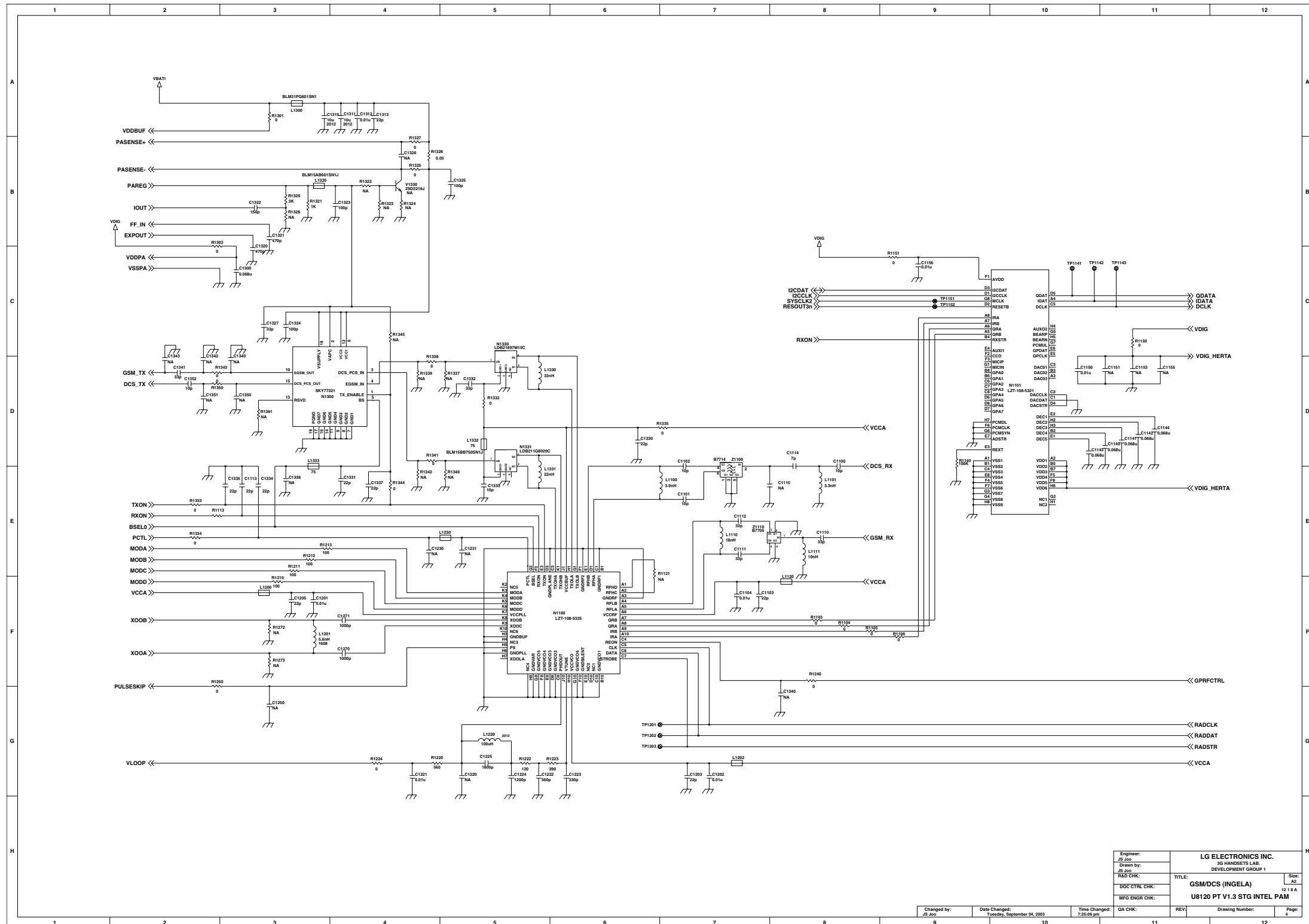
# 9. CIRCUIT DIAGRAM



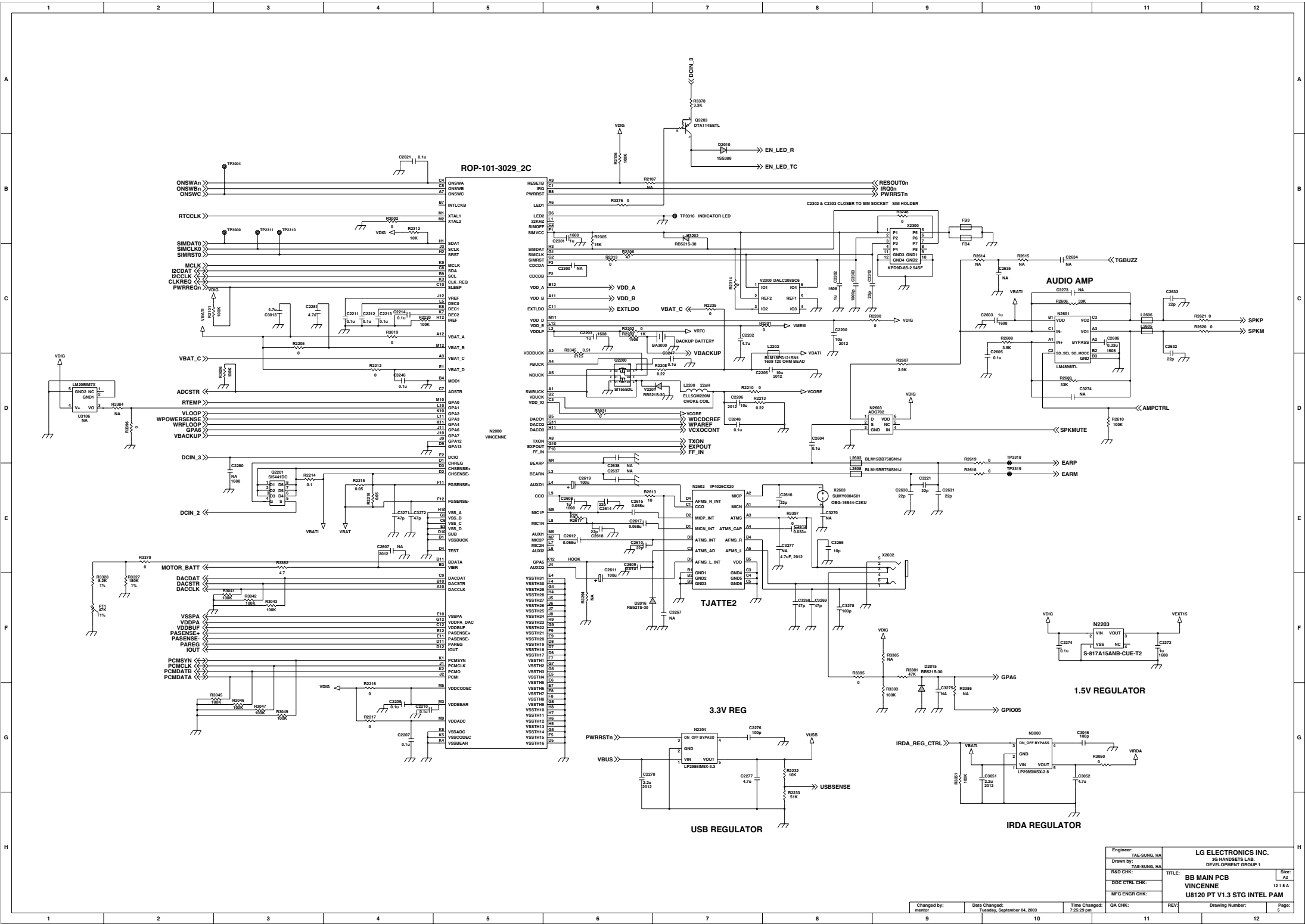
## 9. CIRCUIT DIAGRAM



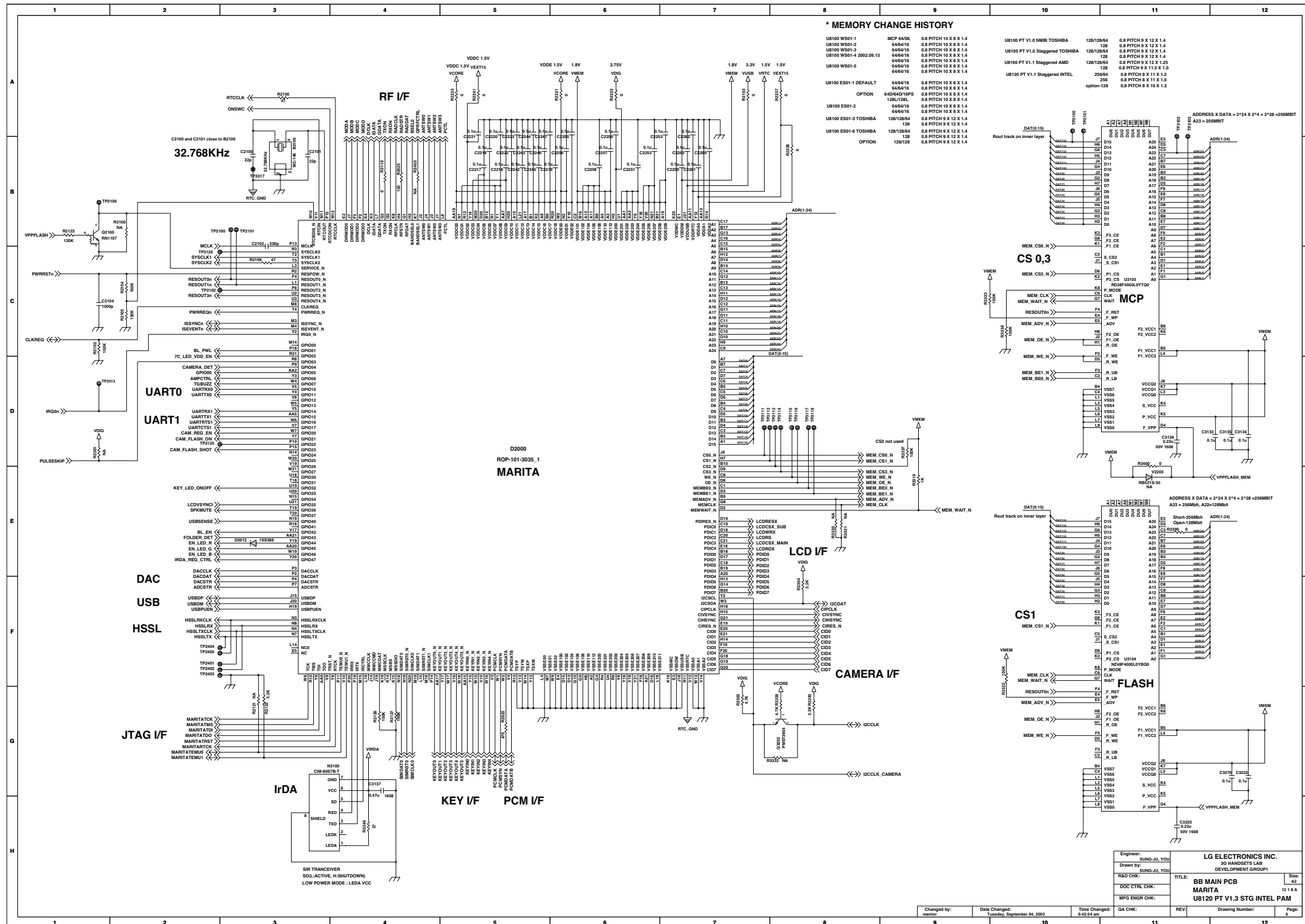
## 9. CIRCUIT DIAGRAM



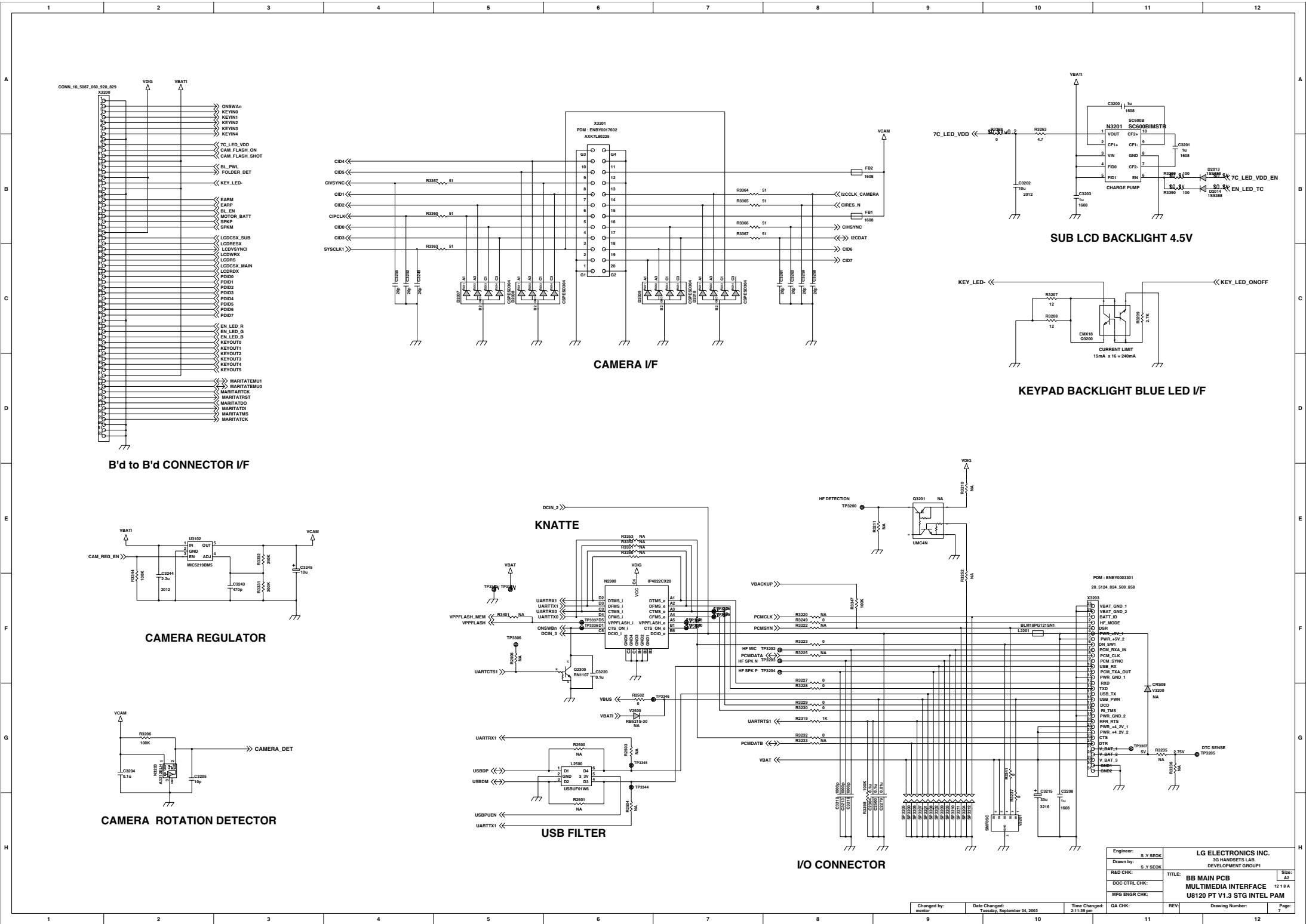
9. CIRCUIT DIAGRAM



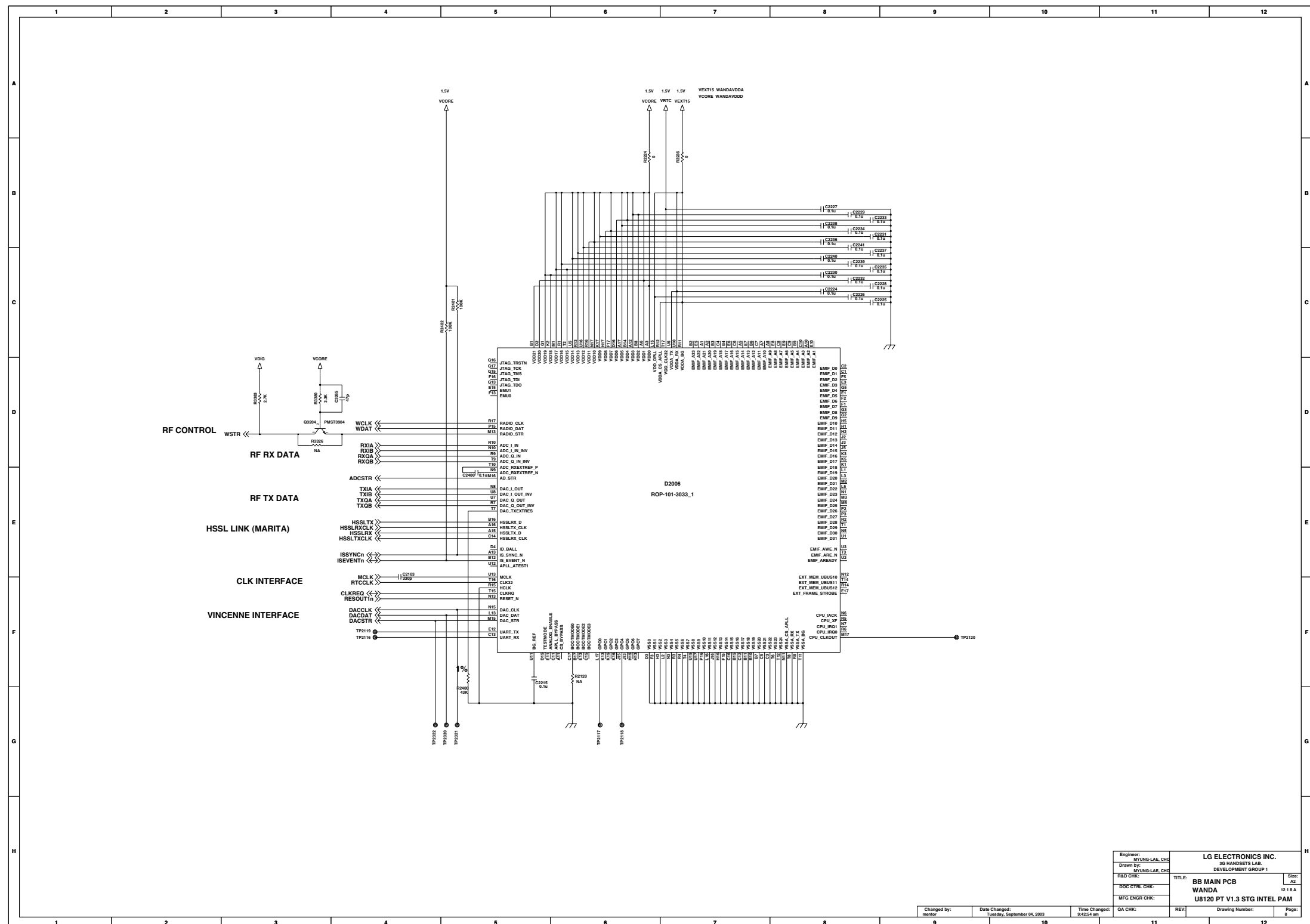
## 9. CIRCUIT DIAGRAM



9. CIRCUIT DIAGRAM

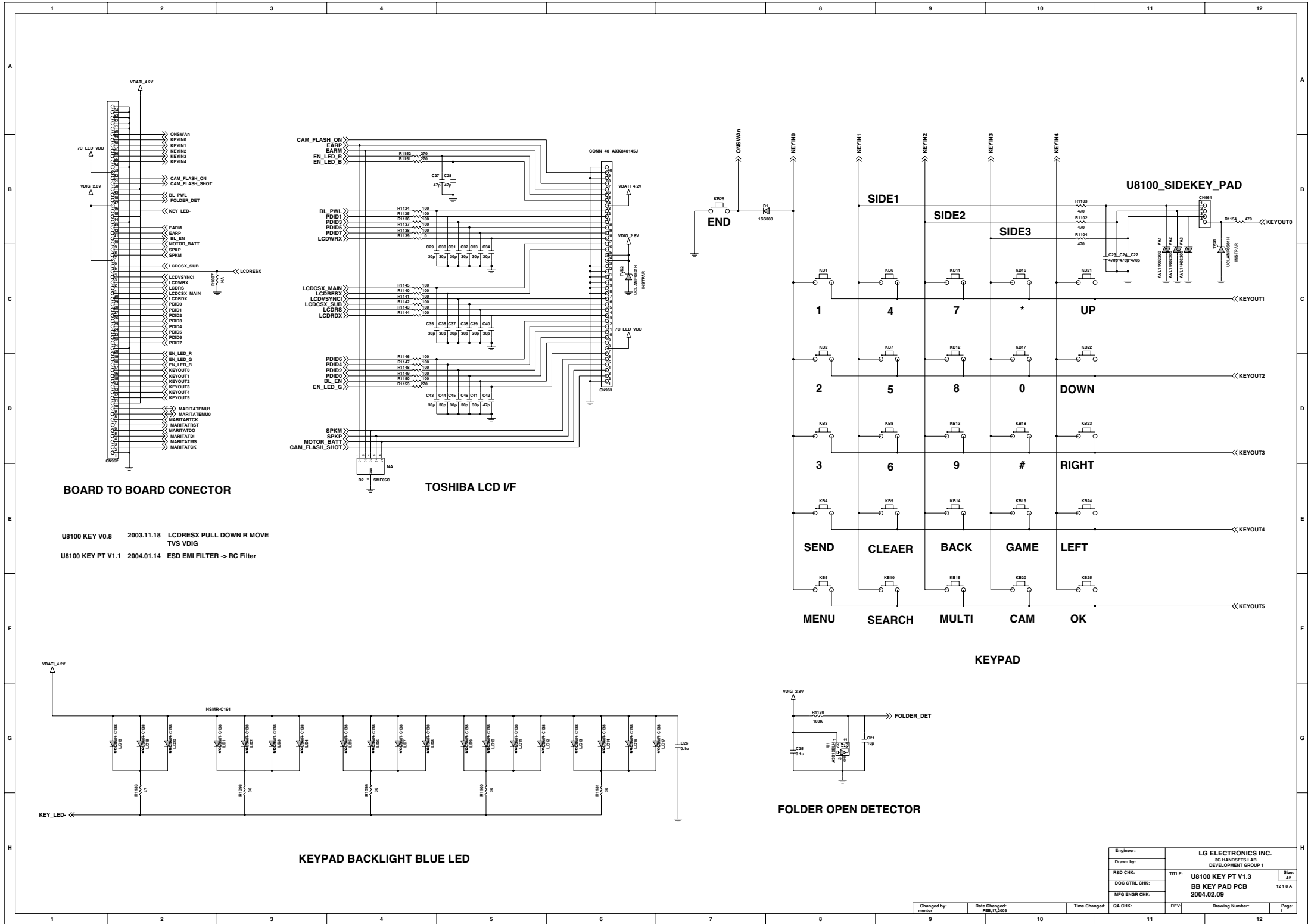


## 9. CIRCUIT DIAGRAM

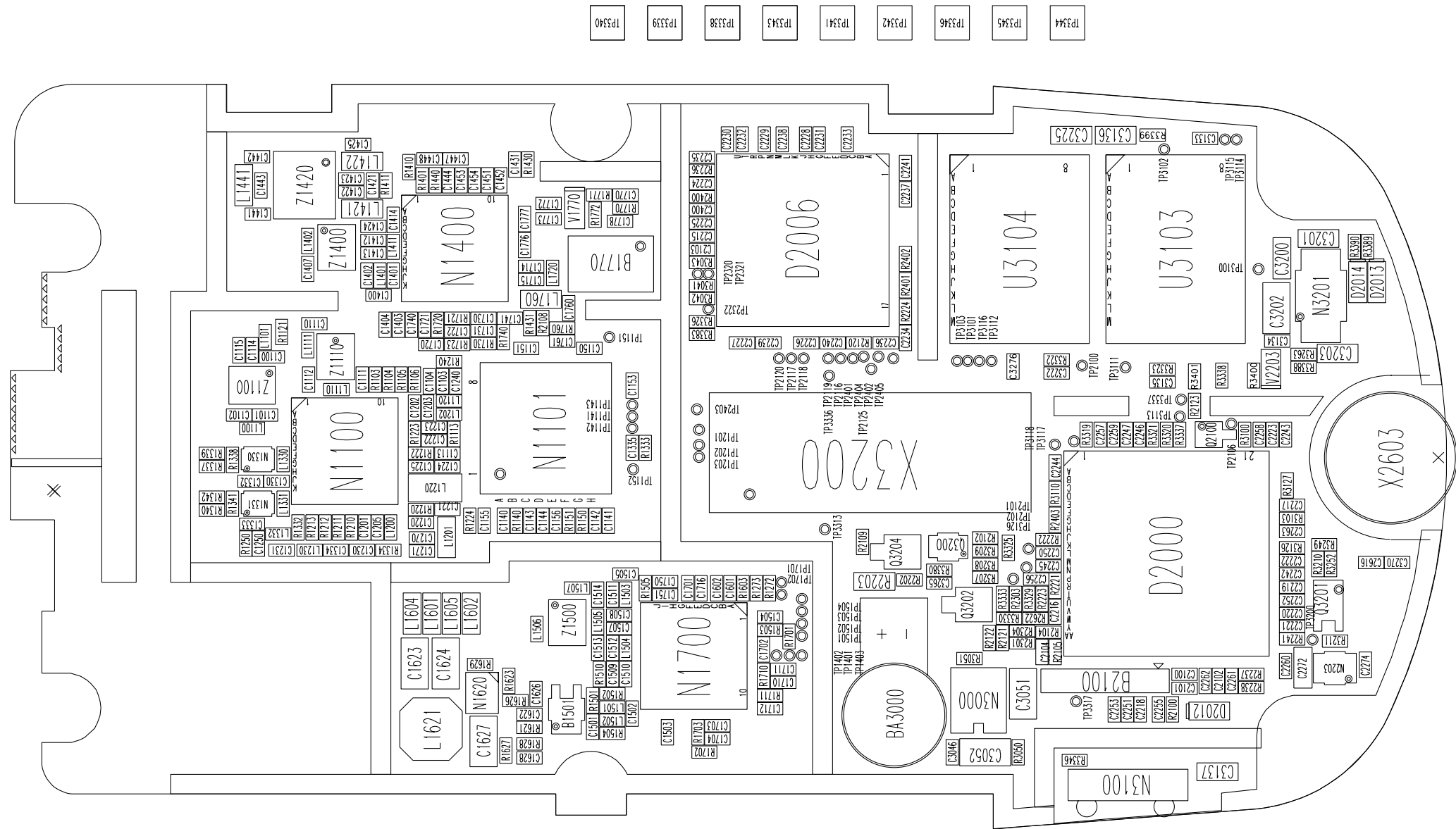




## 9. CIRCUIT DIAGRAM

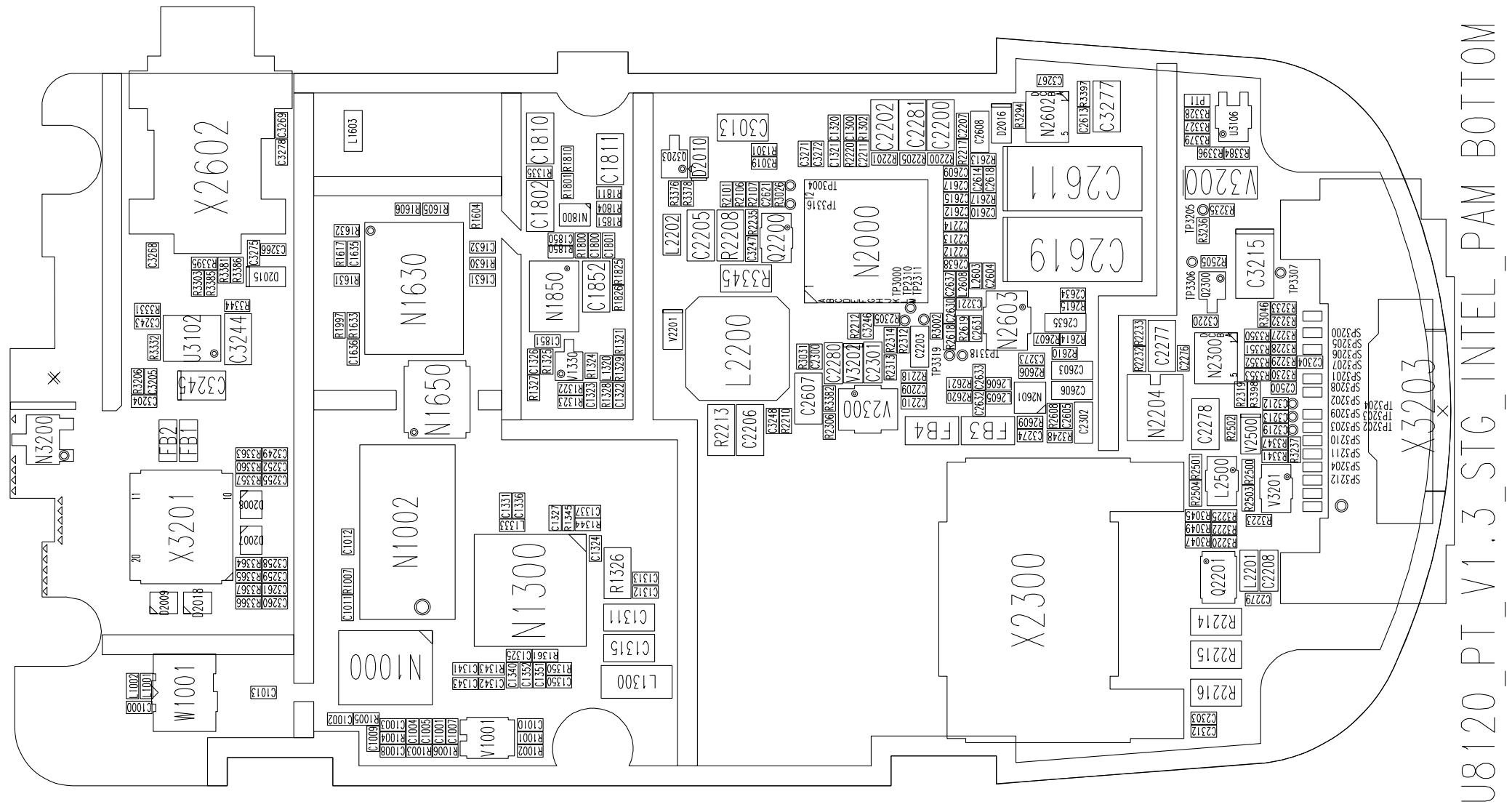


## 10. PCB LAYOUT

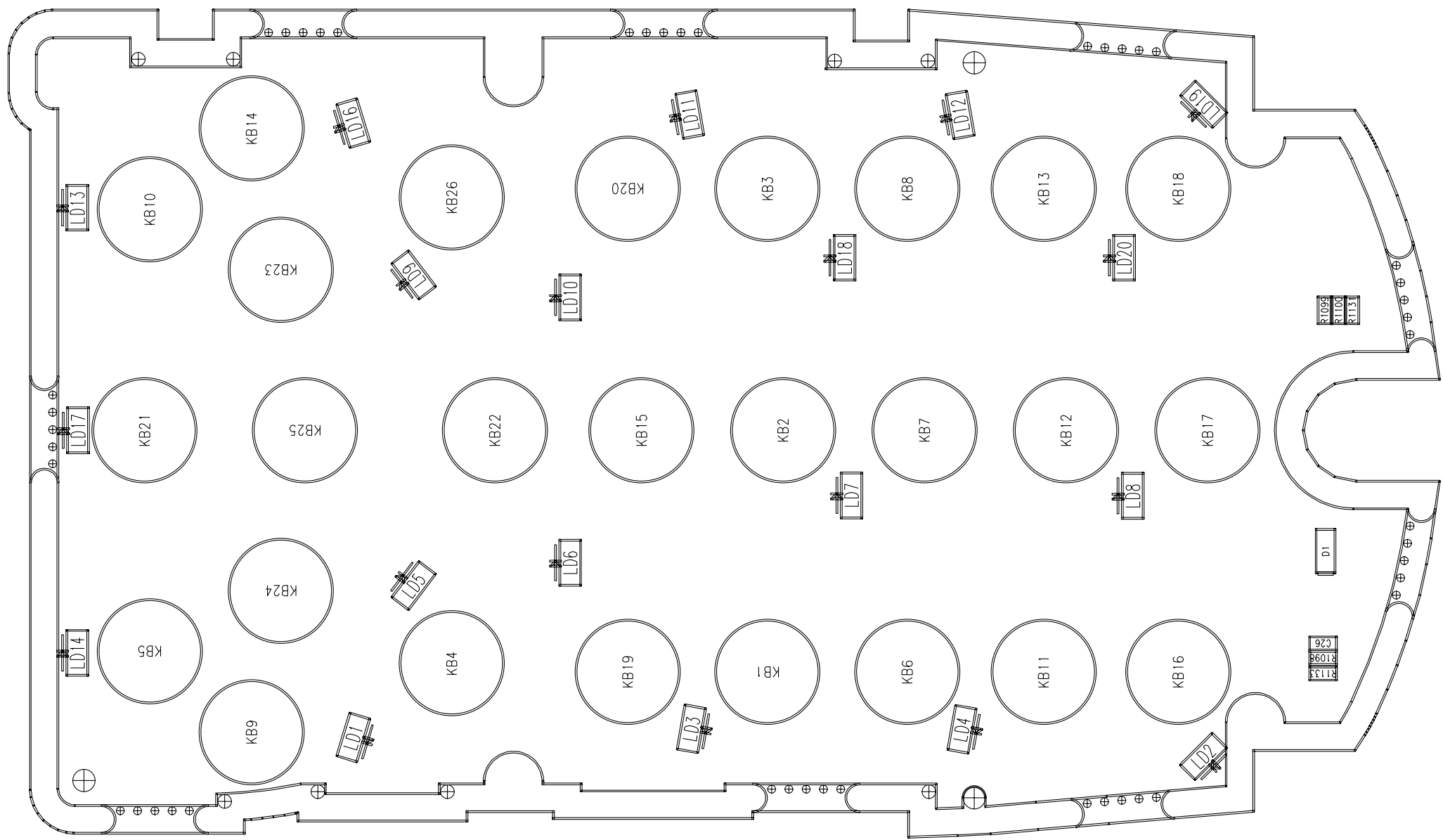


U8120 PT V1.3 STG INTEL PAM TOP

## 10. PCB LAYOUT



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